



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Hemen**

Art Unit: 2815

Serial No.: 09/186,973

Examiner: Cruz L.

Filed: 11/05/98

Docket: TI-23158

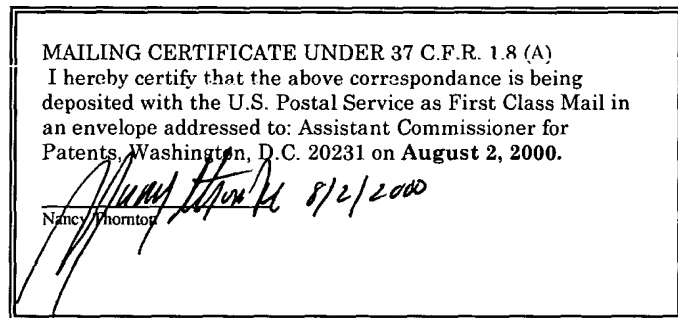
For: **Wafer-Scale Assembly of Chip-Size Packages**

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ELECTION

August 2, 2000

Assistant Commissioner
for Patents
Washington, D.C. 20231



Sir:

Responsive to the Office Action of July 5, 2000, please enter the following election of claims for examination.

Applicants elect Claims 1-13 drawn to a semiconductor device, classified in class 257, subclass 738.

A prompt action on the merits is respectfully requested.

Respectfully Submitted,

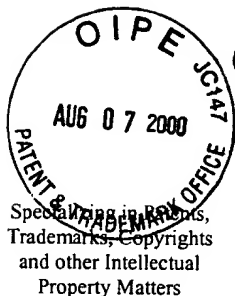
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August 2, 2000

Assistant Commissioner for Patents
Washington, D.C. 20231

Re: Patent Application For:
Wafer-Scale Assembly of Chip-Size Packages
Serial No.: 09/186,973
Attorney Docket No. TI 23158
Our File: 1000-2035

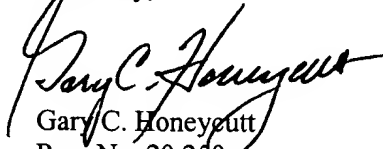
Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Election document; and
- (2) Postcard.

Please file the above documents and return the date-stamped postcard to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely,


Gary C. Honeycutt
Reg. No. 20,250

GCH/ntn
Enclosure

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